Attorney's Docket No.: 10830-048001 / A36-Applicant: Keiji Negi 126904M/YAH

Serial No.: 09/723,194

: November 27, 2000 Filed

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Amendments to the Drawings:

The attached replacement sheets of drawings includes changes to Figs. 7, 8, 9, 10A, 10B, 11 and 12 and replace the previously filed sheets including those same figures.

Figs. 7, 8, 9, 10A, 10B, 11 and 12 have been labeled with the legend "Prior Art."

Various informalities noted by the Examiner have been corrected in Fig. 9 as follows:

- the second entry in column Q1 has been changed to "G"
- the third entry in column Q0 has been changed to "G"

Attachments following last page of this Amendment:

Replacement Sheets (4 pages) Annotated Sheet (1 sheet including Fig. 9)



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Annotated Sheet Showing Change(s)

FIG. 8

TRUTH TABLE OF SHIFT CIRCUIT A (WHERE DATA INPUT IS 16 BITS, AND THERE ARE FOUR SHIFT CIRCUITS)

			INPUT	OUTPUT						
D0	D1	D2	. • • •	D15	SEL	CLK	Q0	Q1	Q2	Q3
Α	В	C		Р	0	†	Α	В	С	D
Α	В	С		Р	1	- †	В	С	D	E
Α	В	С		Р	2	<u>†</u>	С	D	E	F
Α	В	С		Р		†			• • •	• • •
Α.	В	С		Р	15	1	Р	A (+1)	B (+1)	C (+1)

FIG. 9

TRUTH TABLE OF SHIFT CIRCUIT B (WHERE DATA INPUT IS 16 BITS, AND THERE ARE FOUR SHIFT CIRCUITS)

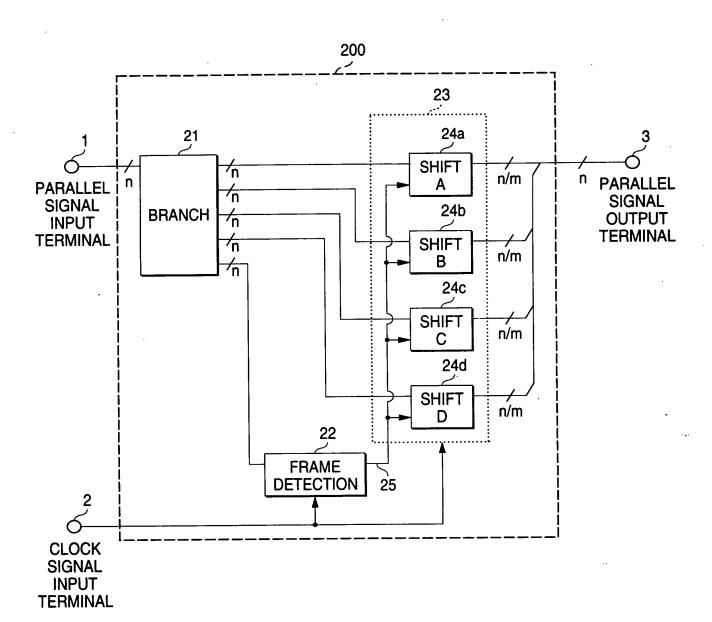
		-	INPUT	OUTPUT						
D0	D1	D2		D15	SEL	CLK	Q0	Q1	Q2	Q3
Α	В	С		Р	0	† .	E	F	G	Н
Α	В	С		Р	1	†	F	X G	Н	. 1
Α	В	С		Р	2	†	G 🕱	Н	1 .	J
Α	В	С		Р		†	• • •	• • •	• • •	
Α	В	С		Ρ	15	Ť	D (+1)	E (+1)	F (+1)	G (+1)

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Prior Art *FIG. 7*





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Prior Art

FIG. 8

TRUTH TABLE OF SHIFT CIRCUIT A (WHERE DATA INPUT IS 16 BITS, AND THERE ARE FOUR SHIFT CIRCUITS)

			INPUT	OUTPUT						
D0	D1	D2		D15	SEL	CLK	Q0	Q1	Q2	Q3
Α	В	С		Р	0	•	Α	В	С	D
Α	В	С		Р	1	Ą	В	С	D	E
Α	В	С		Р	2	A	С	D	Е	F
A	В	С		Р		A				
Α	В	С		Р	15	Ą	Р	A (+1)	B (+1)	C (+1)

Prior Art FIG. 9

TRUTH TABLE OF SHIFT CIRCUIT B (WHERE DATA INPUT IS 16 BITS, AND THERE ARE FOUR SHIFT CIRCUITS)

			INPUT	OUTPUT						
D0	D1	D2		D15	SEL	CLK	Q0	Q1	Q2	Q3
Α	В	С		Р	0	A	E	F	G	Н
Α	В	С		Р	1	A	F	G	Н	<u> </u>
Α	В	С		Р	2	A	G	Н	1	J
Α	В	С		Р		†				• • •
Α	В	С		Р	15	4	D (+1)	E (+1)	F (+1)	G (+1)



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Prior Art FIG. 10A

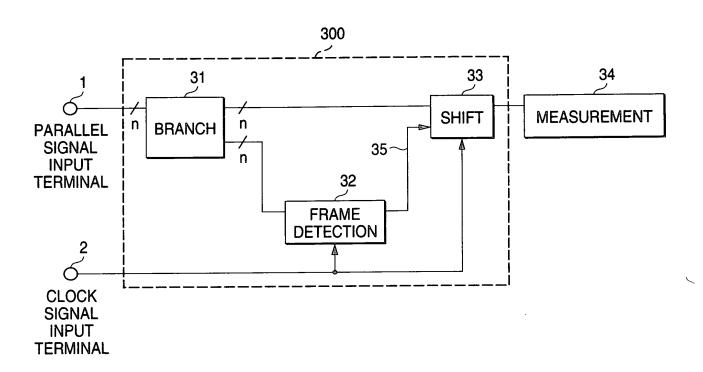
Prior Art *FIG. 10B*

DATA OUTPUT DATA INPUT SHIFT A OUTPUT j (-1) X а X BIT0 X X b X X k (-1) BIT1 X X X C X BIT2 1(-1) X X d m (-1) X X BIT3 X SHIFT B OUTPUT X е X n (-1) BIT4 X f X X o (-1) BIT5 X g X X BIT6 p (-1) X X **FRAME POSITION SIGNAL** X h X BIT7 а X X į X Χ BIT8 b X X X 0 X BIT14 h X X i X р X BIT15 X X



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Prior Art FIG. 11



Prior Art FIG. 12

TRUTH TABLE OF SHIFT CIRCUIT (WHERE DATA INPUT IS 16 BITS)

			•	OUTPUT							
D0	D1	D2		D15	SEL	CLK	Q0	Q1	Q2		Q3
Α	В	С		Р	0	Ŷ	Α	В	С		Р
Α	В	С		Р	1	Ŷ	В	С	D		A (+1)
Α	В	С		Р	2	Ą	С	D	Е		B (+1)
Α	В	· C		Р		Ŷ			•••		
Α	В	С		Р	15	Ą	Р	A (+1)	B (+1)		O (+1)